Docket No. ALLEG-042PUS

Appl. No. 10/696,230

REMARKS

Applicant respectfully requests the Examiner to consider and enter the following Remarks under the provisions of 37 C.F.R §1.312.

Applicant would like to point out an unintentional error in a paragraph beginning on page 14 of the Remarks section of the Amendment submitted on January 28, 2005 in which recurring patterns of states were denoted by underlining. The underlining should have been shown as indicated below. Furthermore, one state was incorrectly indicated as corrected below.

Operation of the circuit of FIGS. 4 and 5 in the claimed first mode of operation, a buck-boost mode of operation (characterized by an input voltage close to a regulated output voltage), can be understood by referring to FIGS. 13-13B. In FIG. 13, it can be seen that the states transition in a sequence AC, BC, AC, AC, AD, AC, AC, BC, AC, AC, AD, AC, where the underlined portion corresponds to a repeating pattern of states when an error voltage, VCONT, is in a first voltage range. In FIG. 13A, it can be seen that the states transition in a sequence AC, BC, AC, AC, BC, AC, AC, AD, AC, AC, BC, AC, where the underlined portion corresponds to a repeating pattern of states when the error voltage, VCONT, is in a second voltage range. In FIG. 13B, it can be seen that the states transition in a sequence AC, AD, AC, AC, AD, AC, AC, BC, AC, AC, ADBC, AC, where the underlined portion corresponds to a repeating pattern of states when the error voltage, VCONT, is in a third voltage range. Therefore, in the first mode of operation, it will be understood that, to provide regulation, adjustment is made to the *number of state transitions from the first state to the* second state relative to a number of state transitions from the first state to the third state.